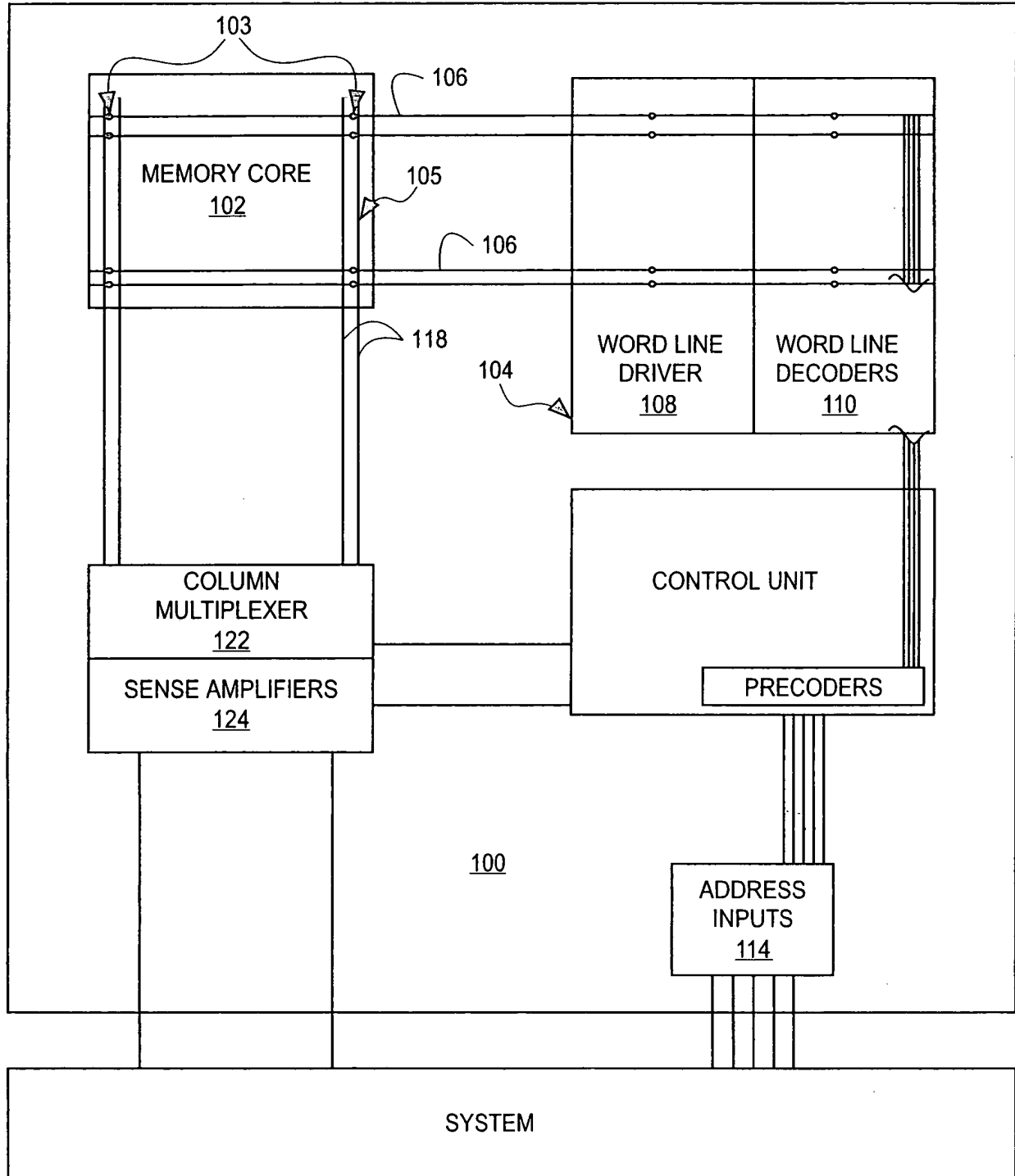


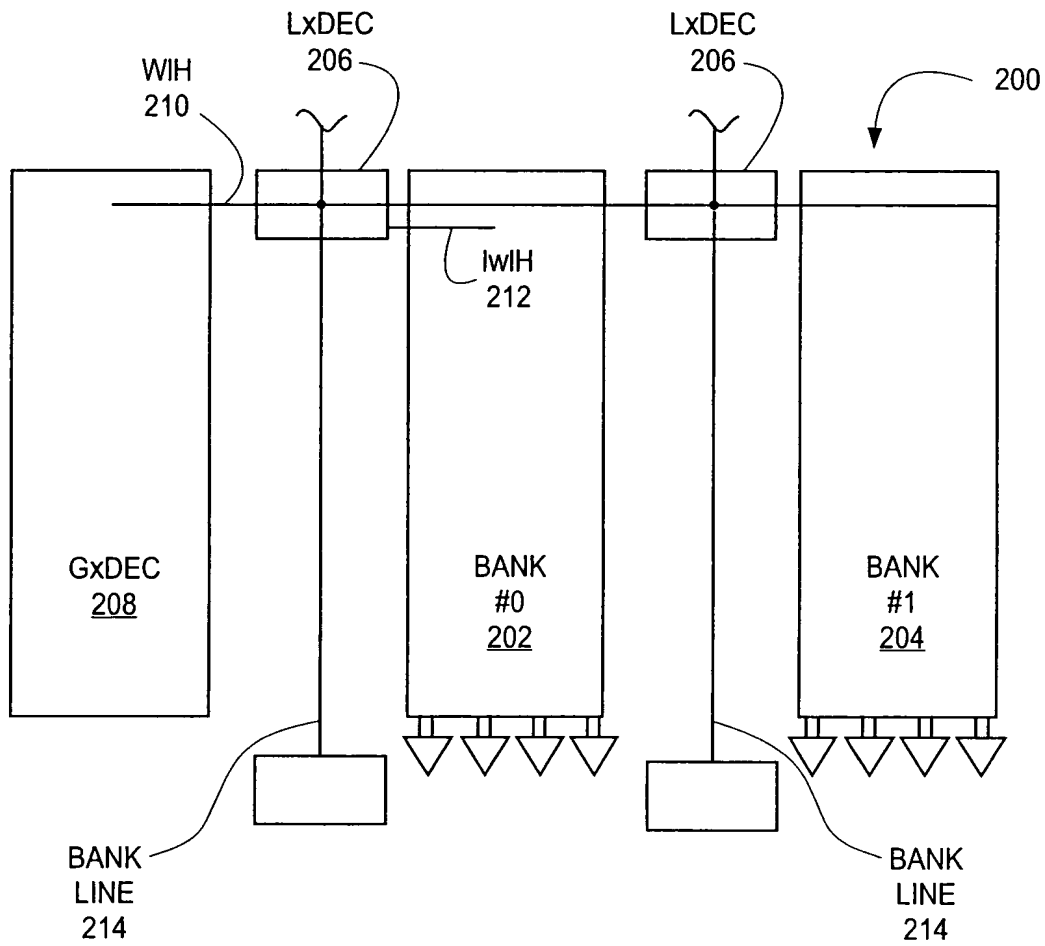
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FIG. 1



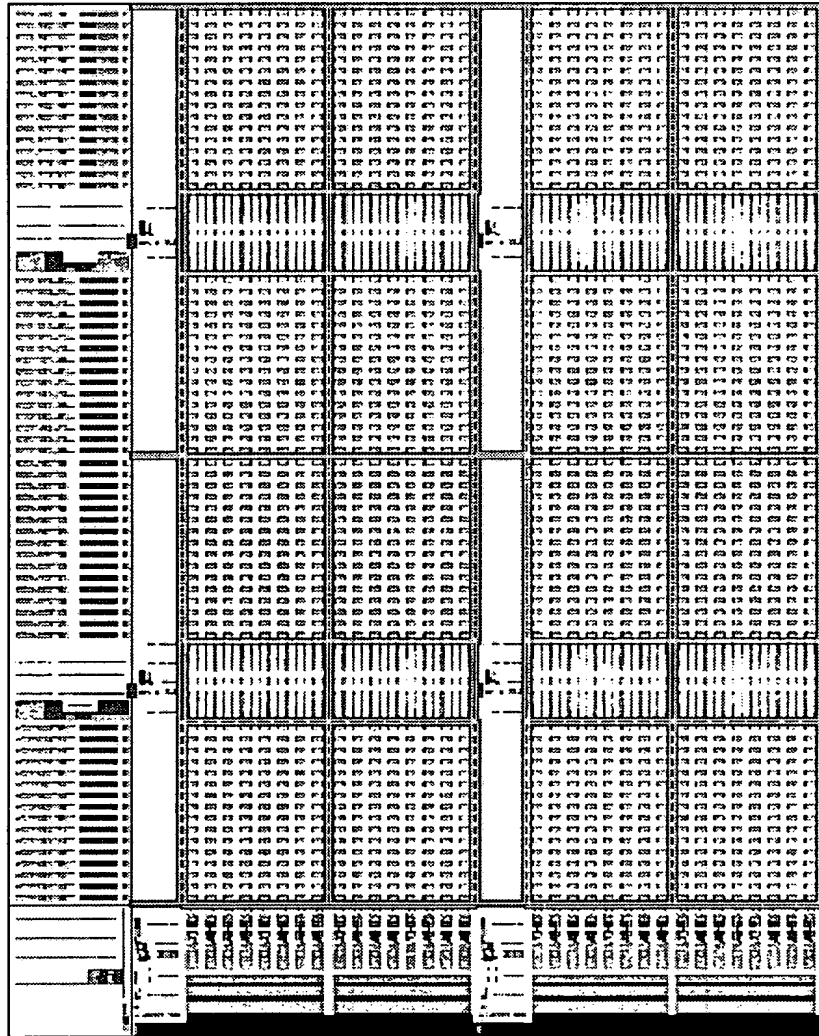
2/26

FIG. 2



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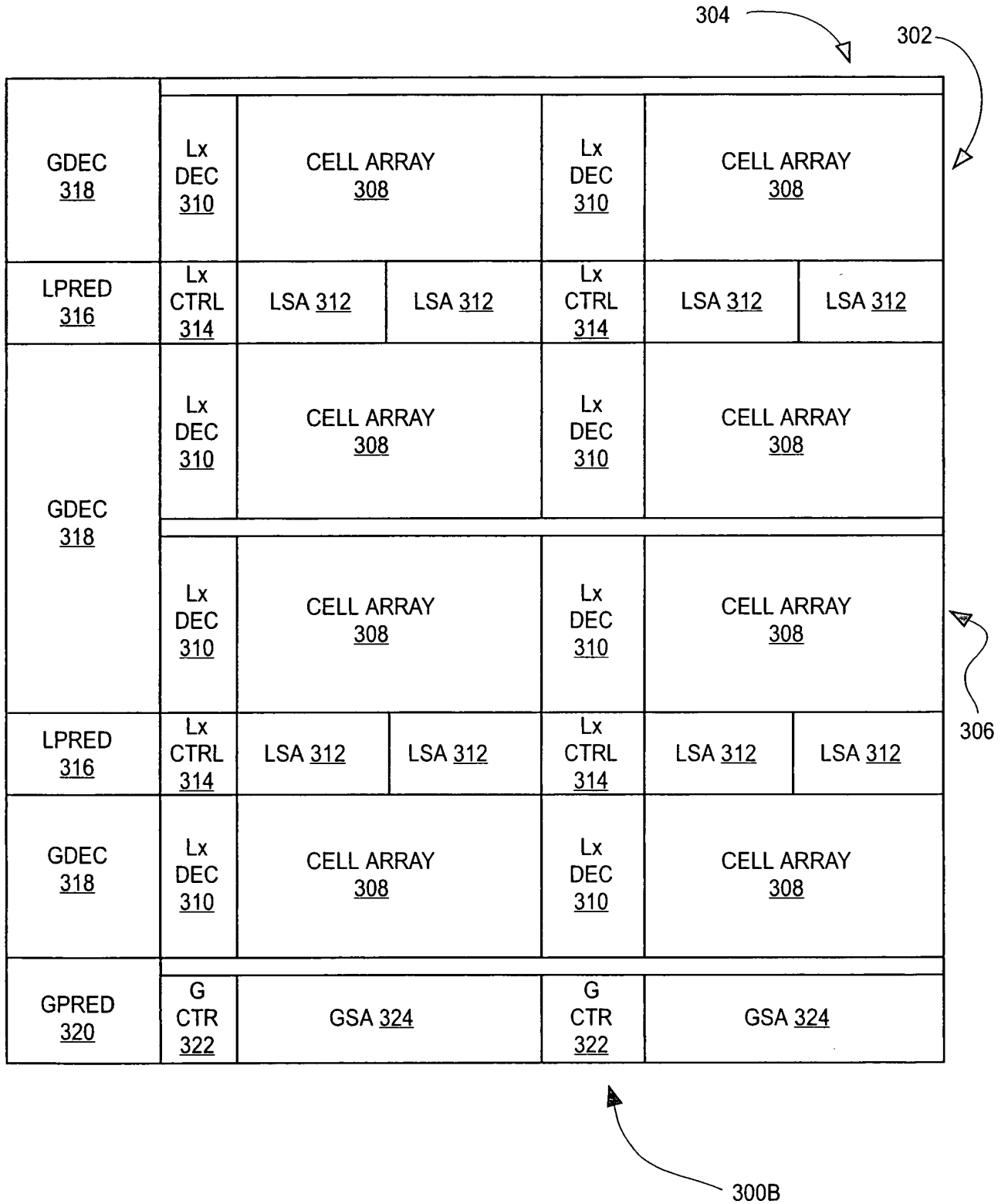
FIG. 3A



300A

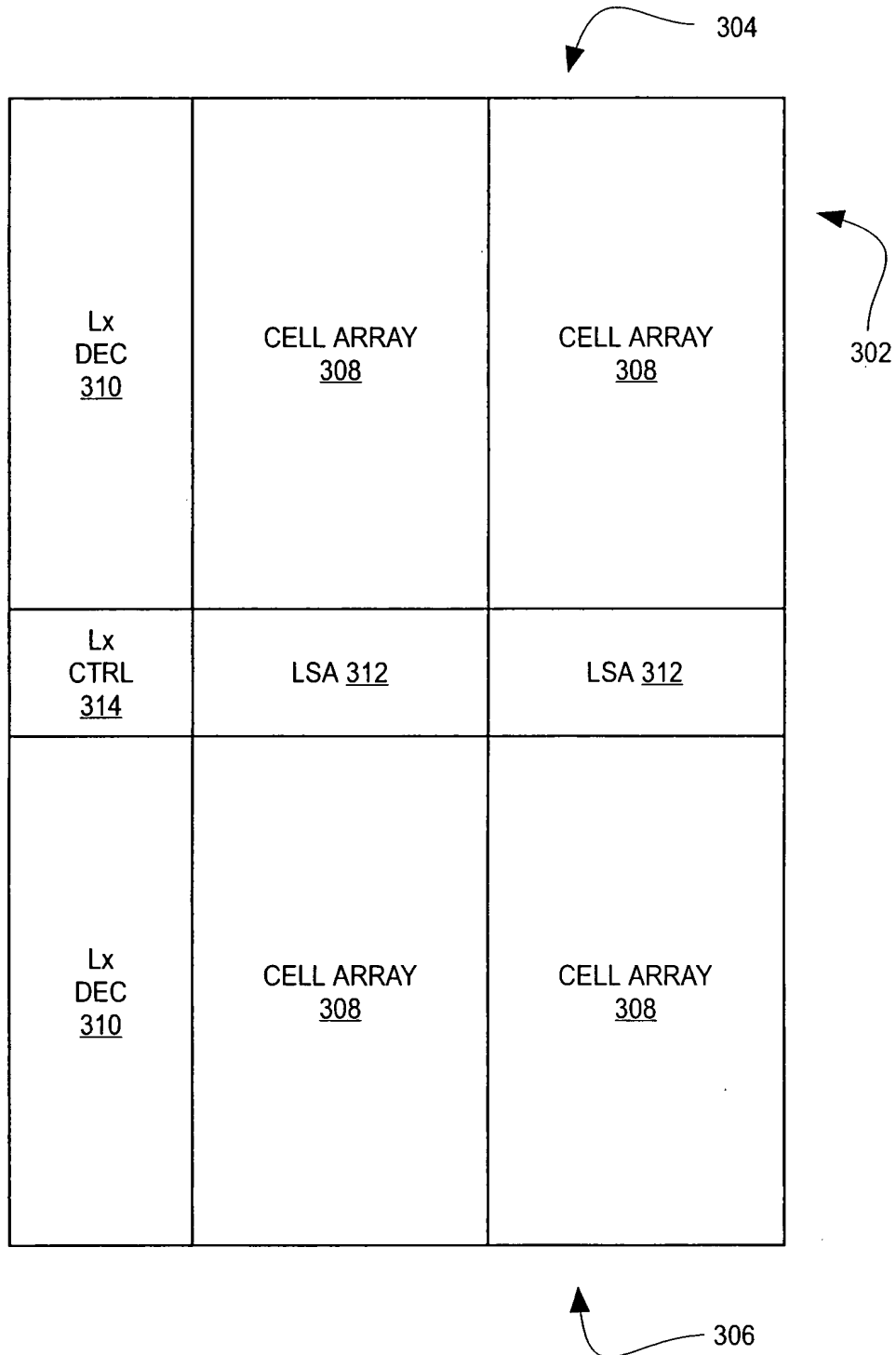
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FIG. 3B



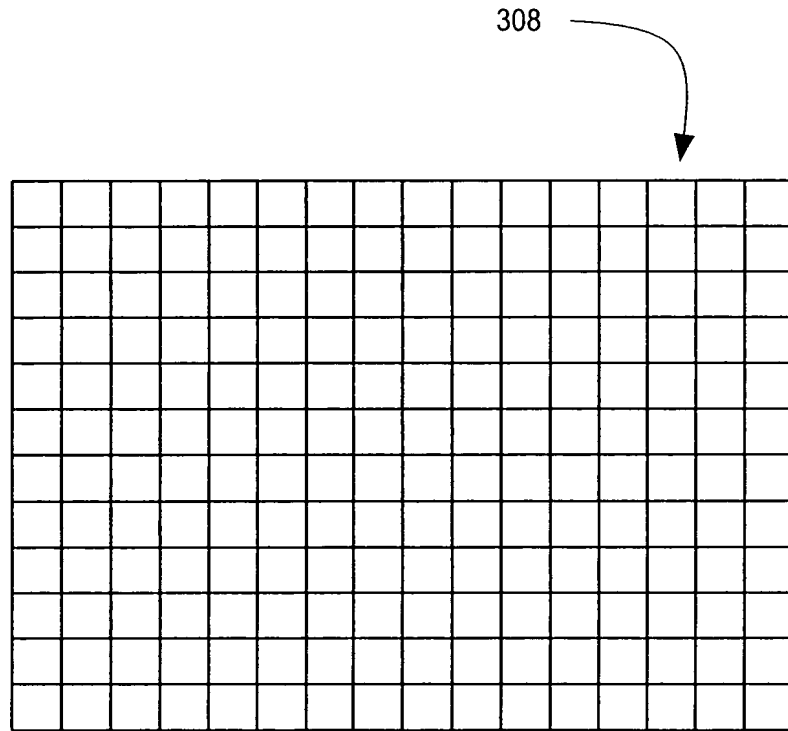
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FIG. 4



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FIG. 5



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FIG.6A

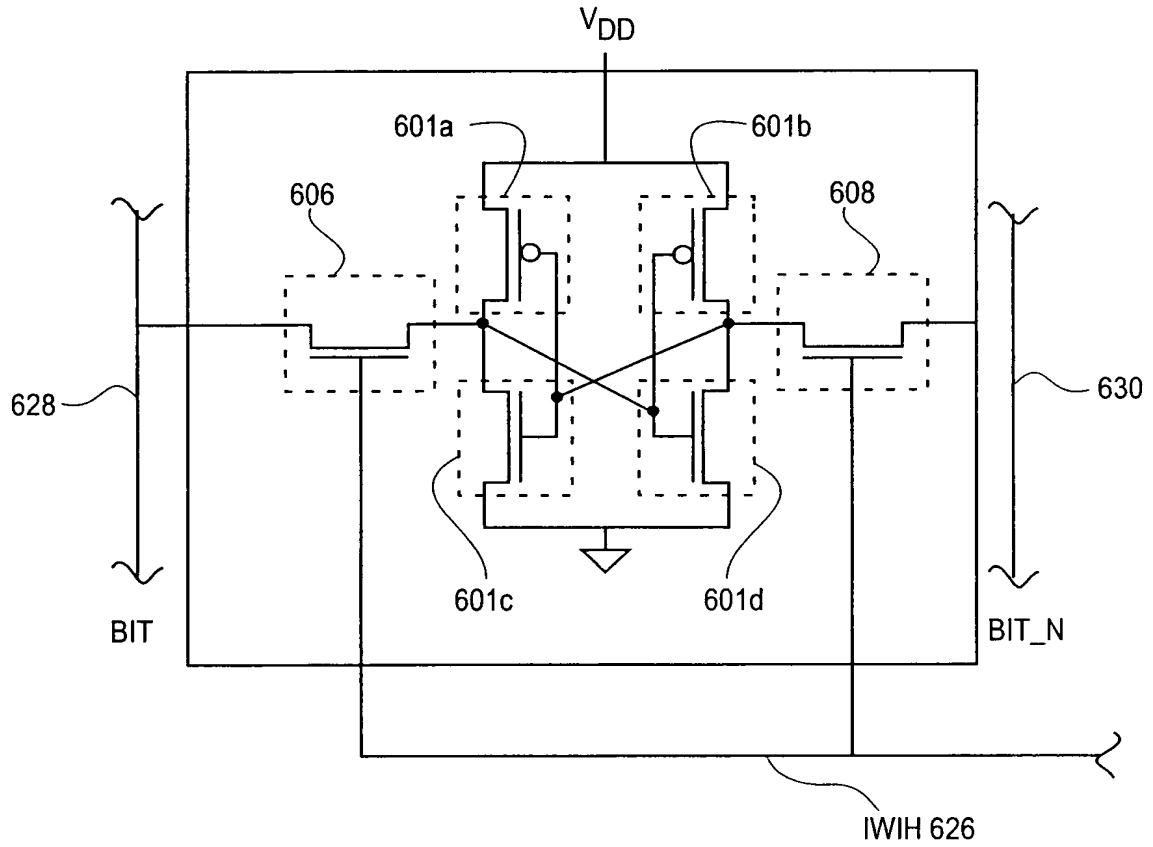
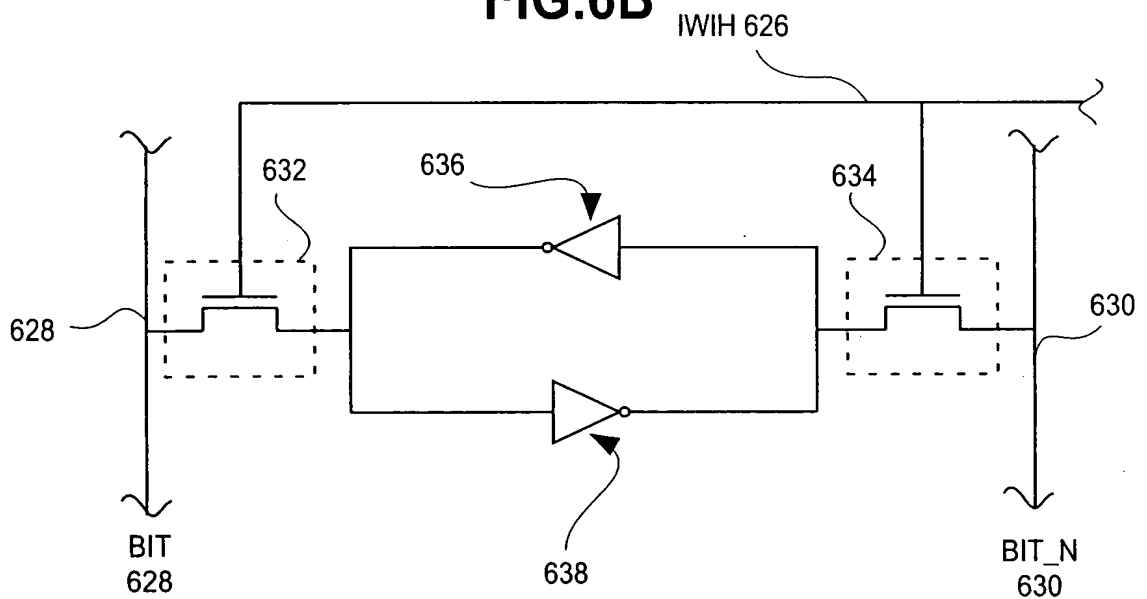
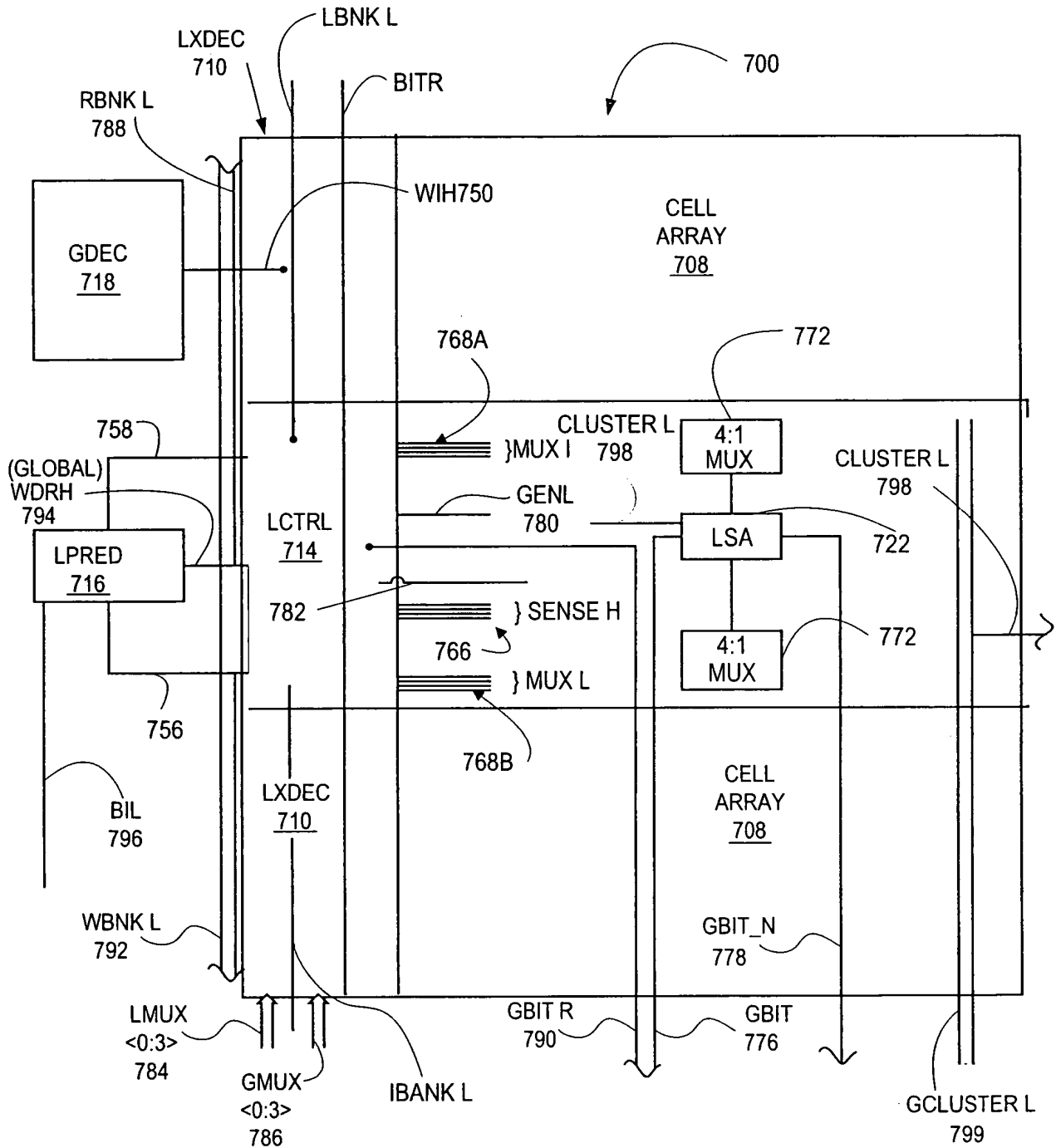


FIG.6B



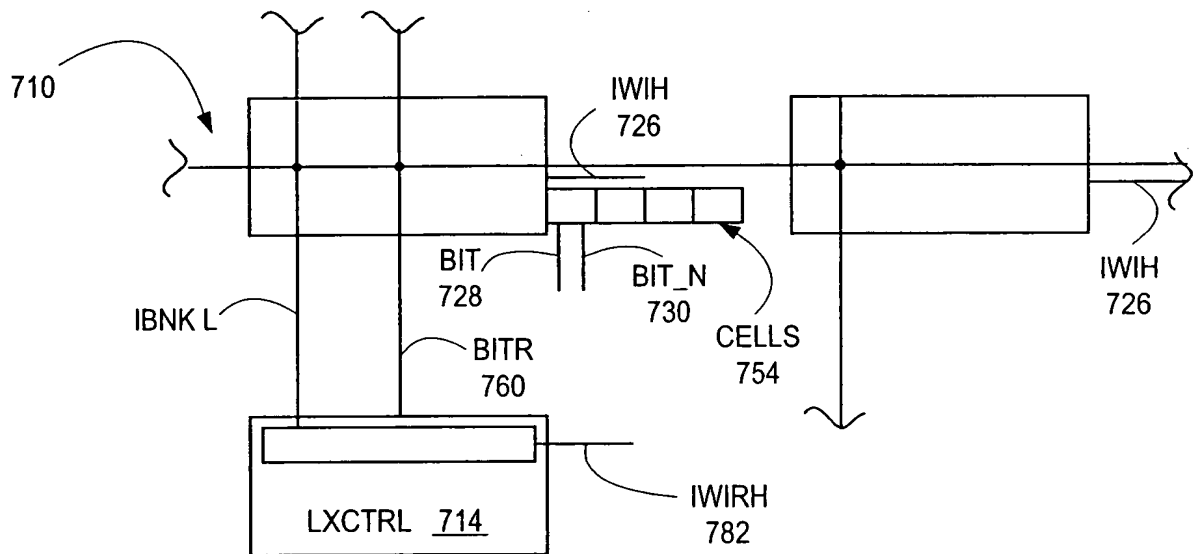
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FIG. 7



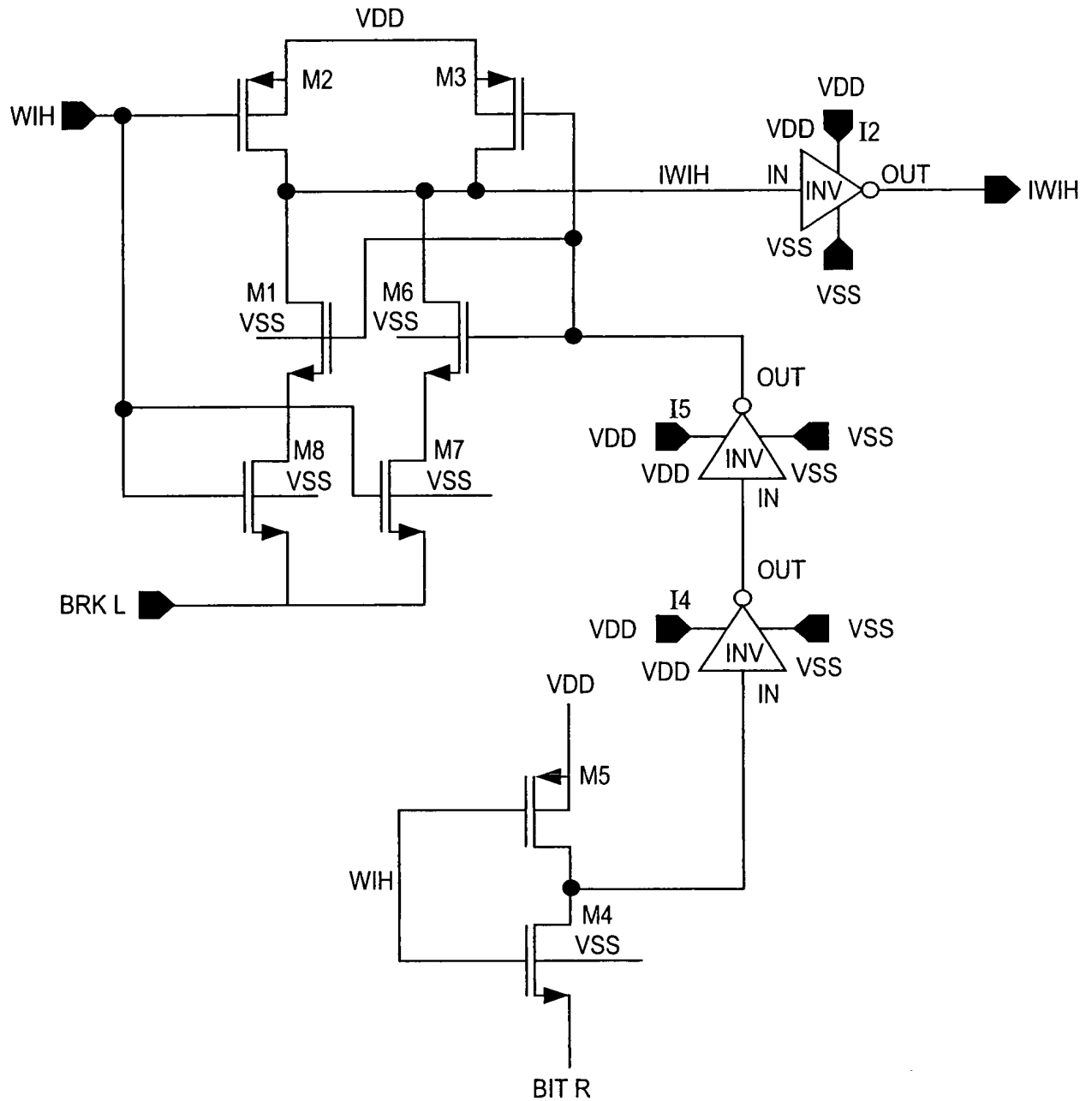
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FIG. 8



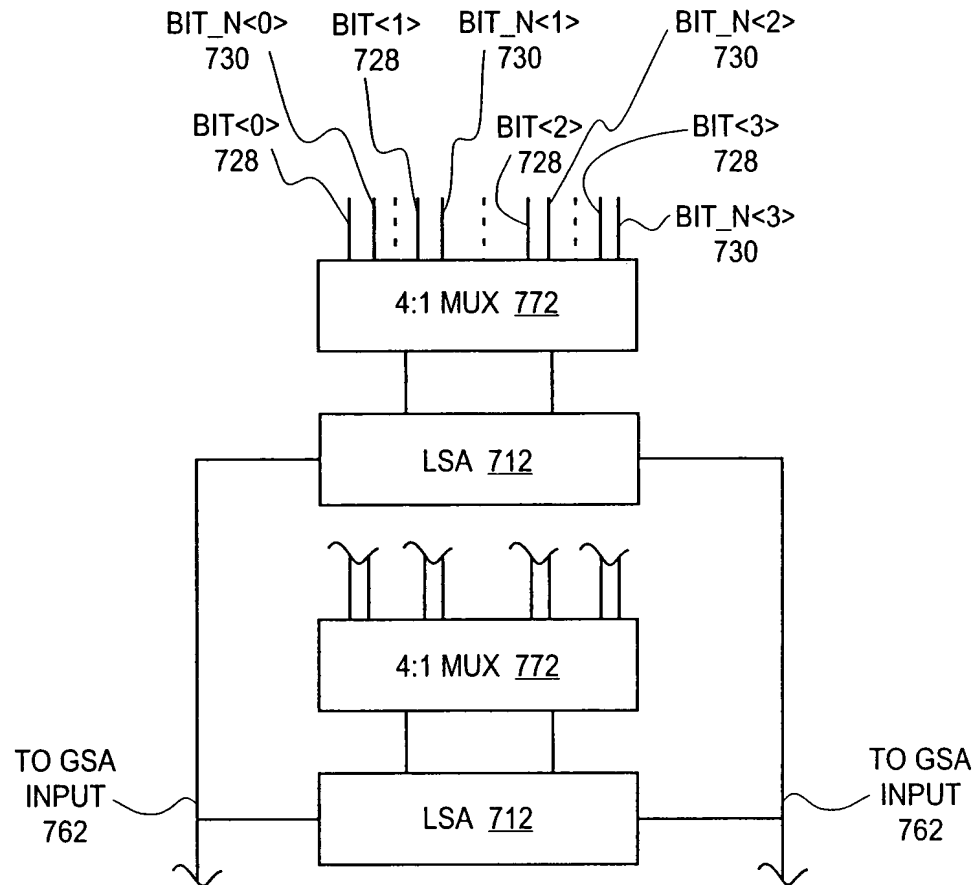
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FIG. 9



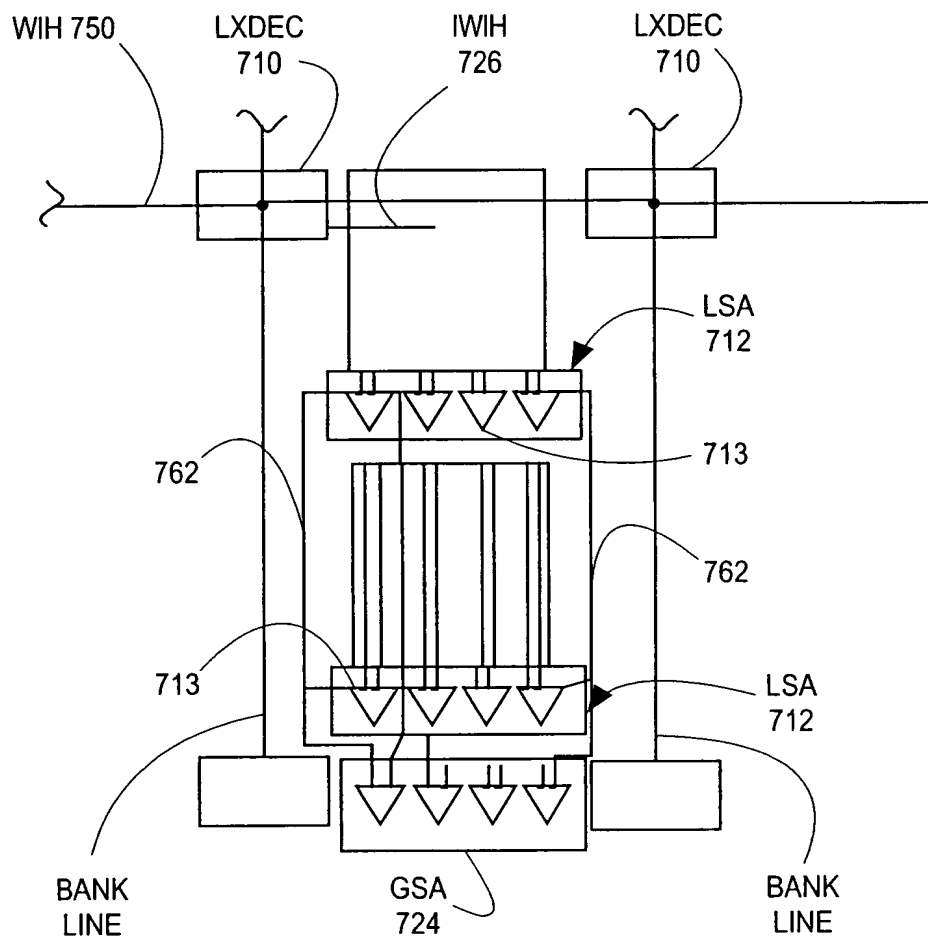
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FIG. 10



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FIG.11



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FIG. 12B

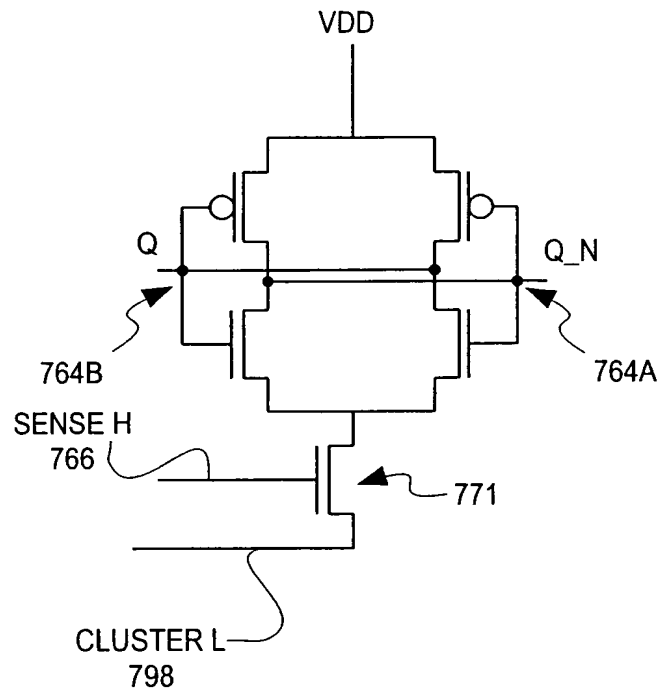
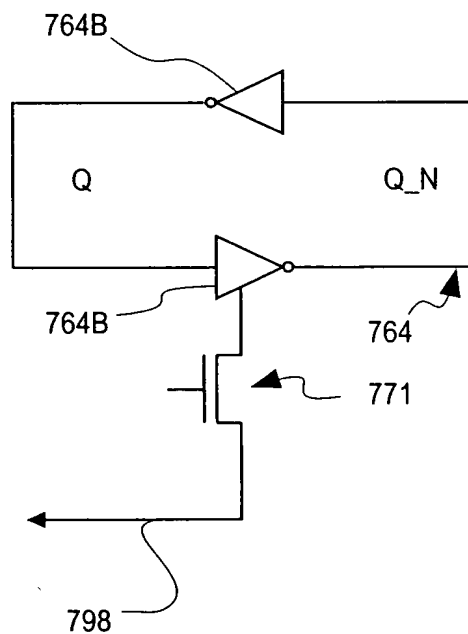
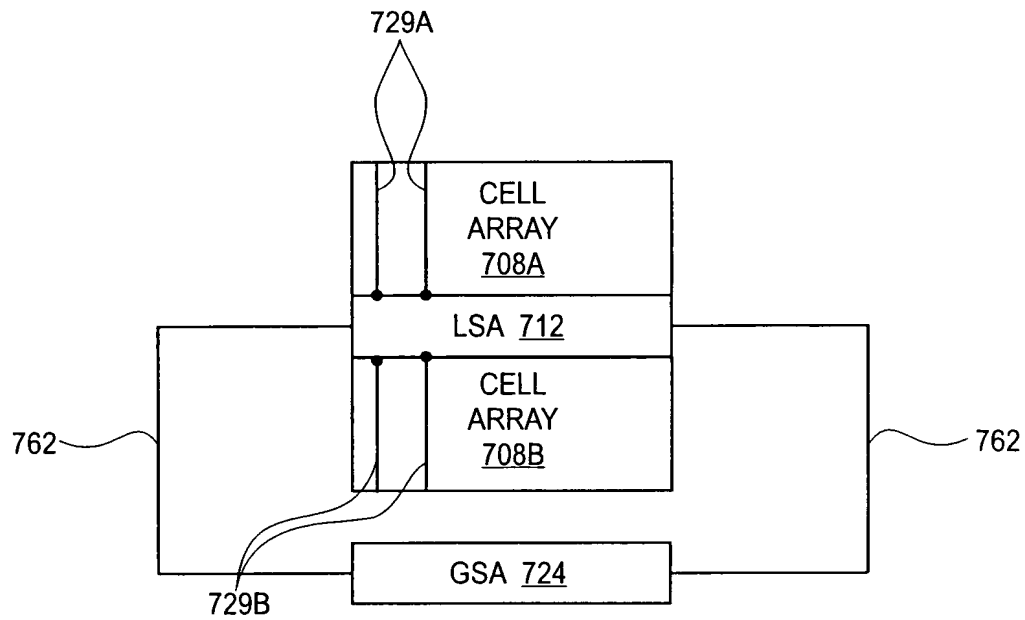


FIG. 12C



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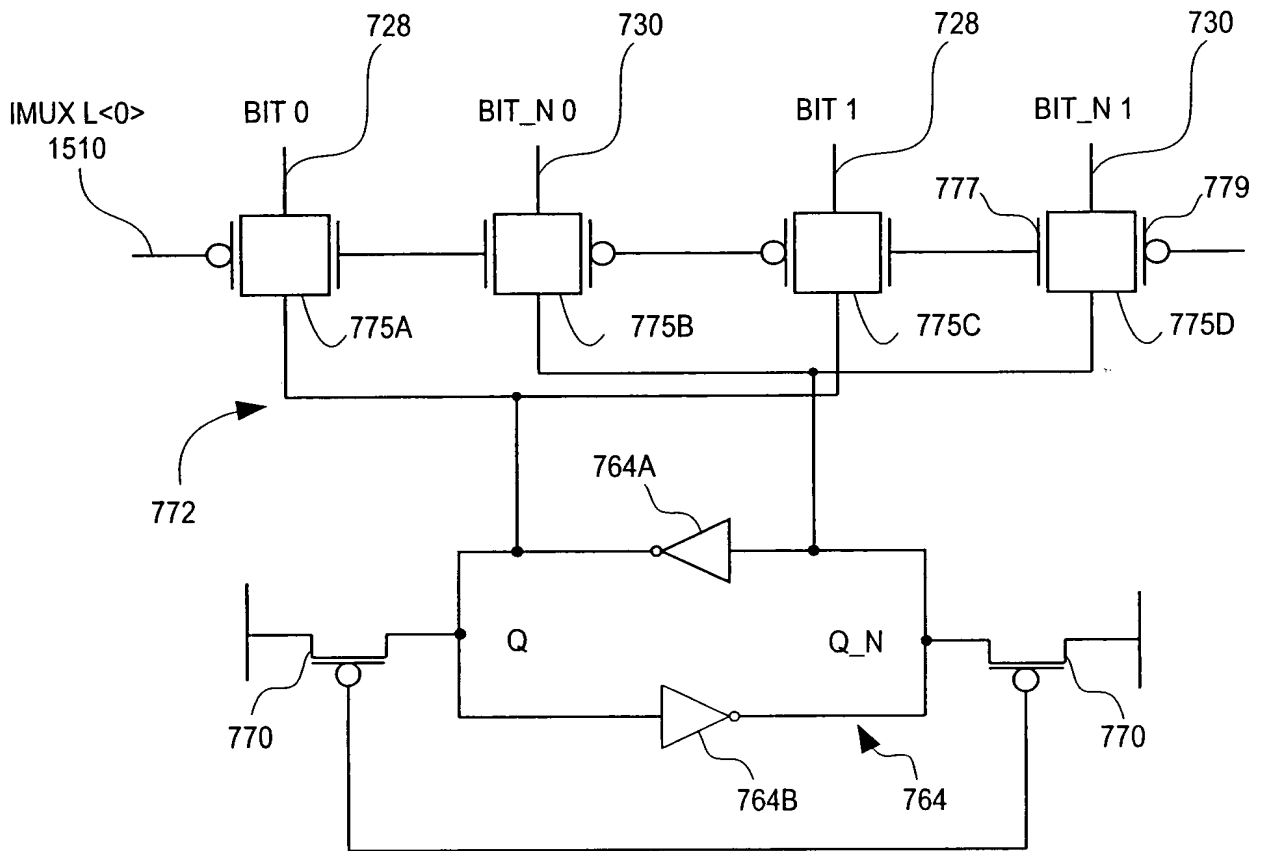
FIG. 13



[illegible]

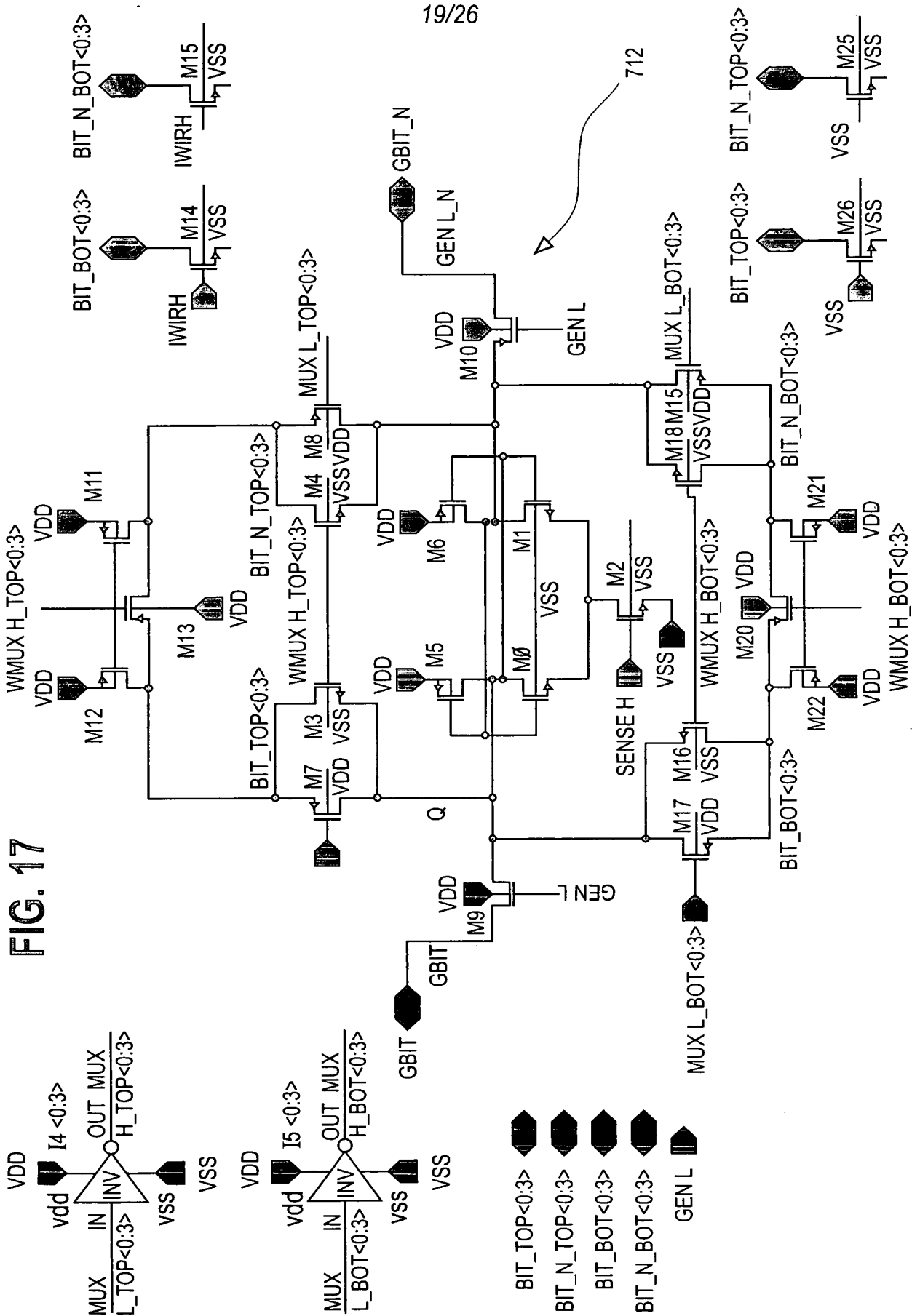
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FIG. 15



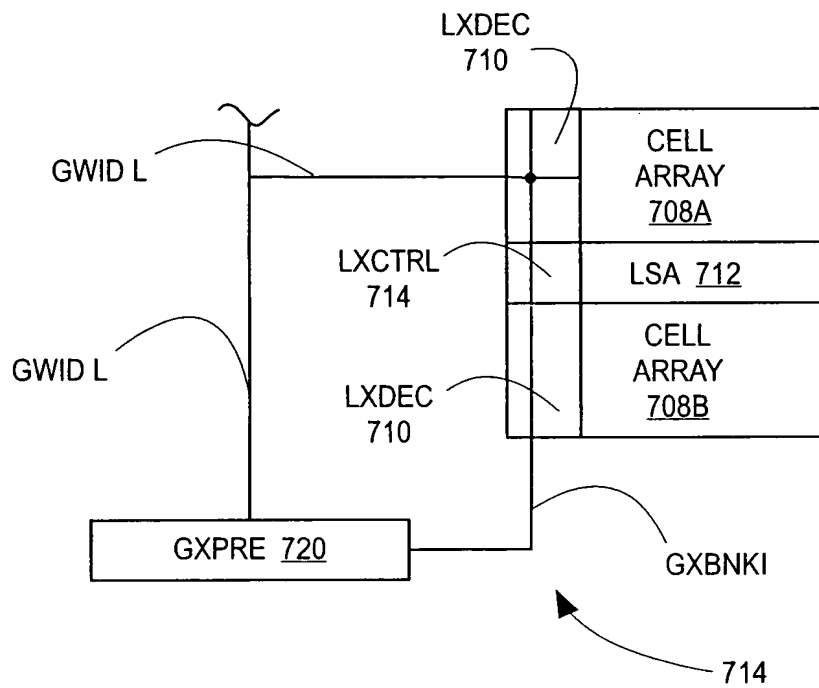
The diagram illustrates a multi-bit input multiplexer (773) and a crossbar switch (764). The multiplexer 773 has multiple inputs labeled IMUX L<0> 1610 and IMUX H<0> 1614, and multiple outputs labeled BIT_N<0> 730 and BIT_N<1> 730. It includes a control signal BIT<0> 728. The circuit is composed of several transistors (773A, 773B, 773C, 775A, 775B, 775C, 775D) and a buffer (1612). The crossbar switch 764 is a 2x2 switch with inputs Q and Q_N, and outputs Q and Q_N. It includes a control signal BIT<0> 728 and a buffer (1612). The circuit is powered by VDD and ground.

17
17
G.
E.



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FIG. 18



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FIG. 19

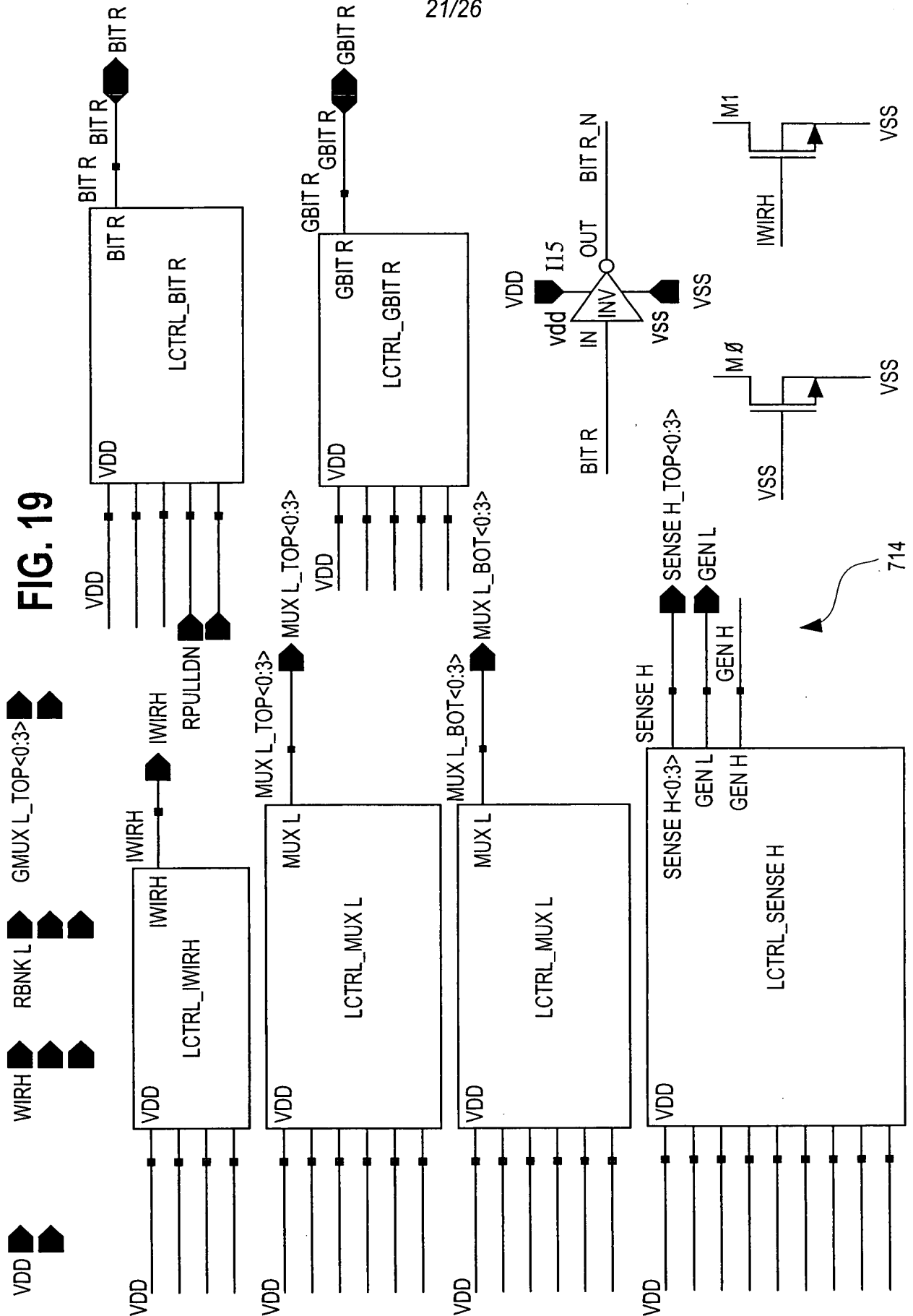
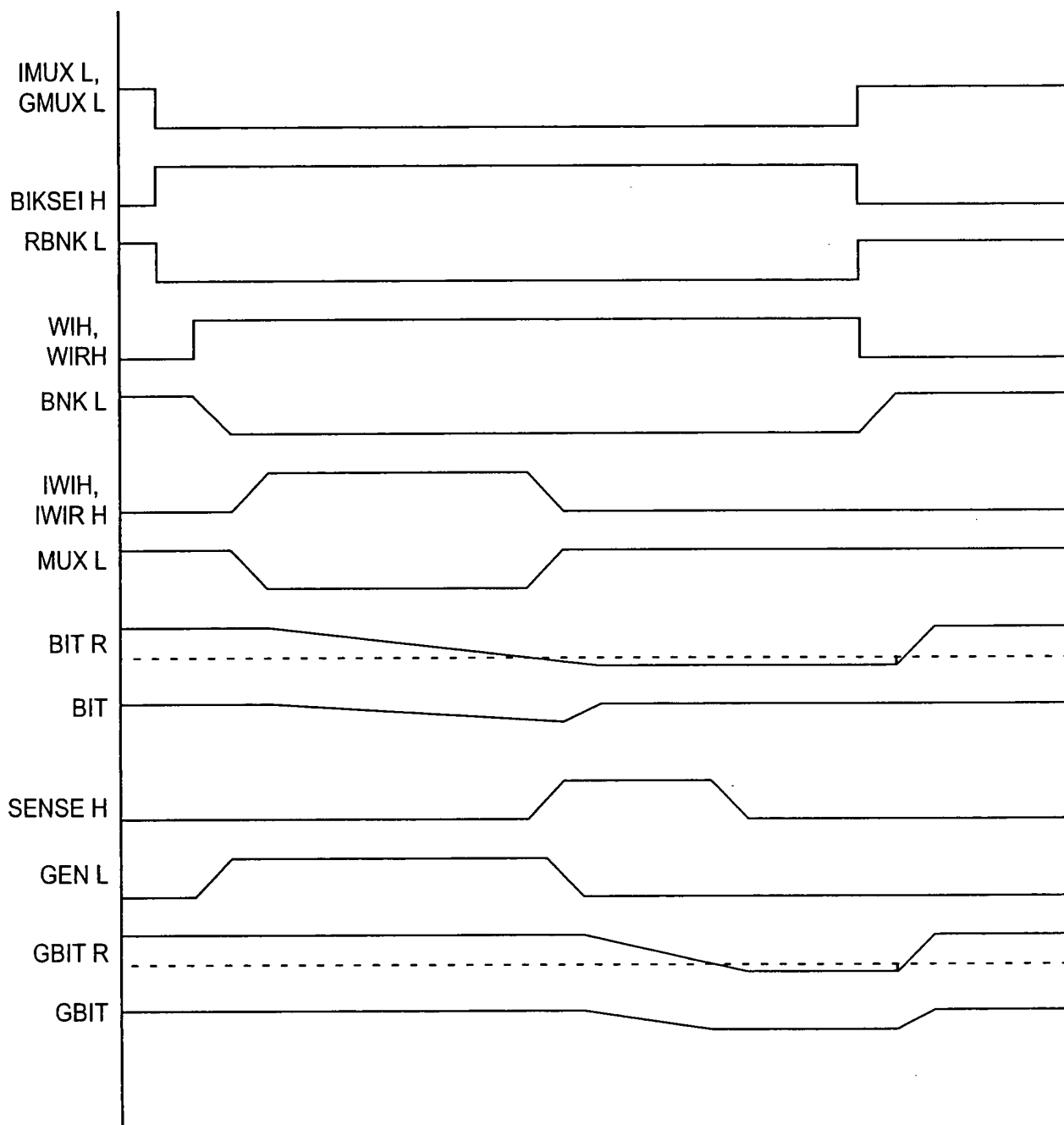
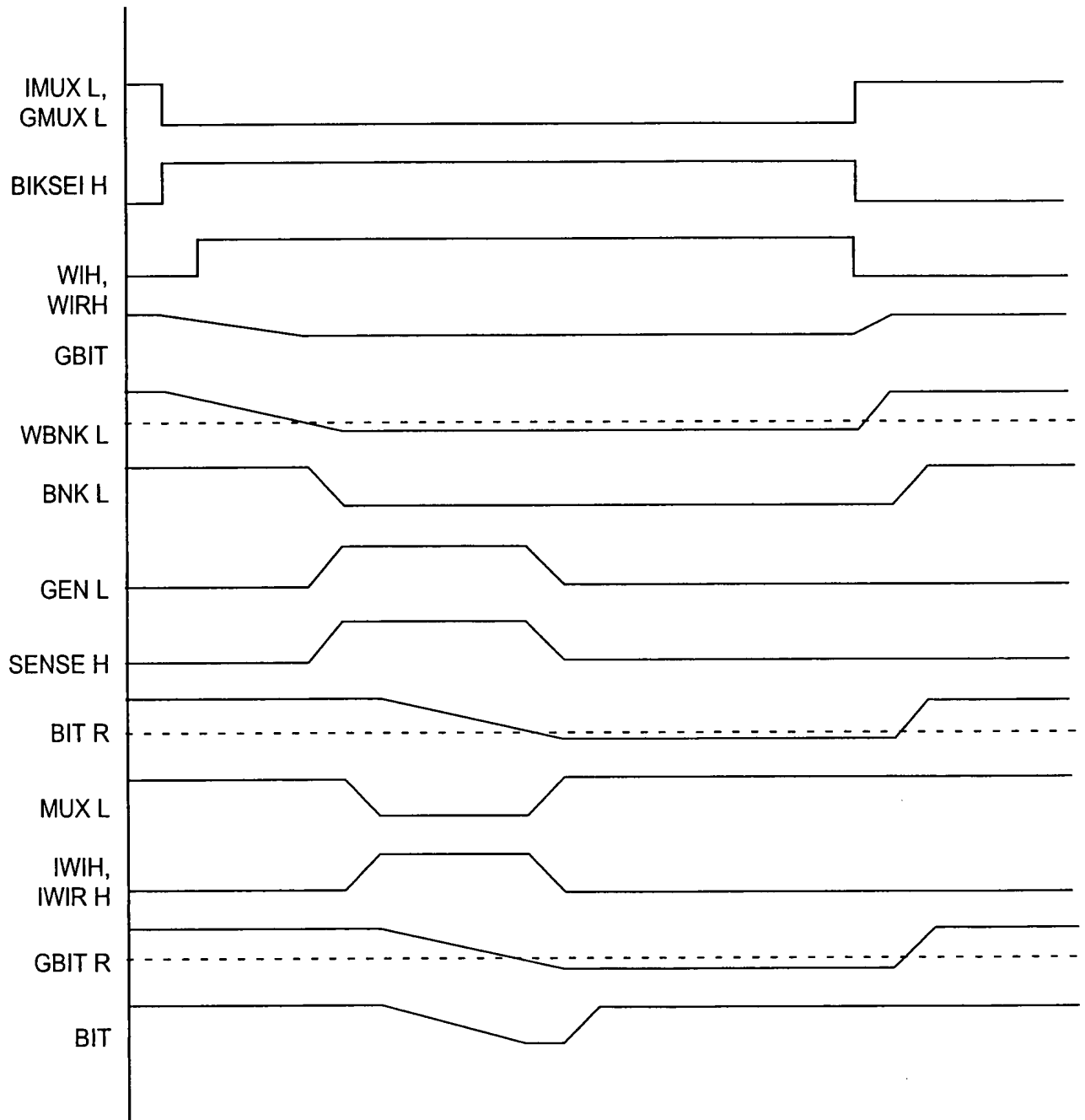


FIG. 20



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FIG. 21



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FIG. 22A

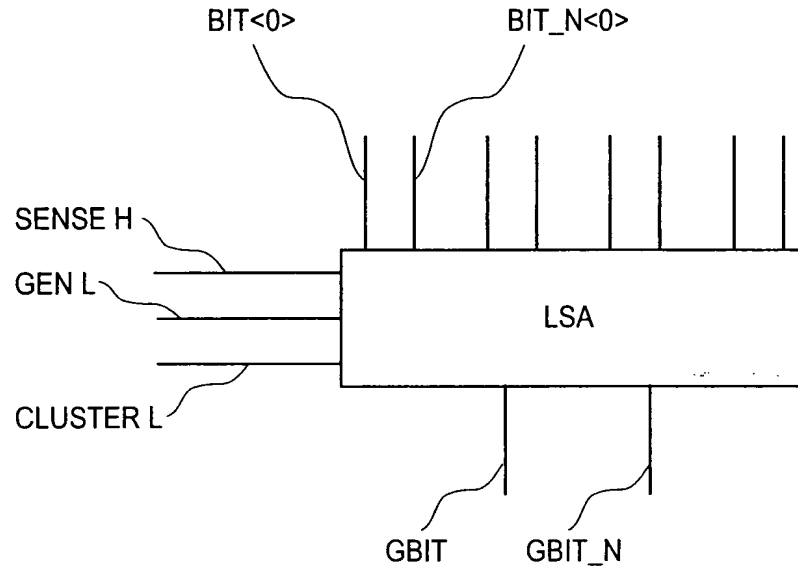
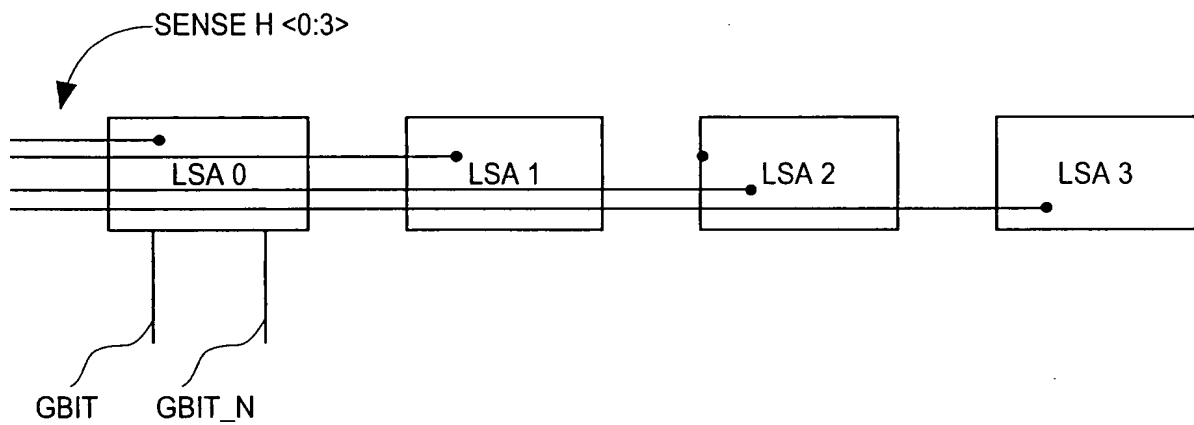


FIG. 22B



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FIG. 22C

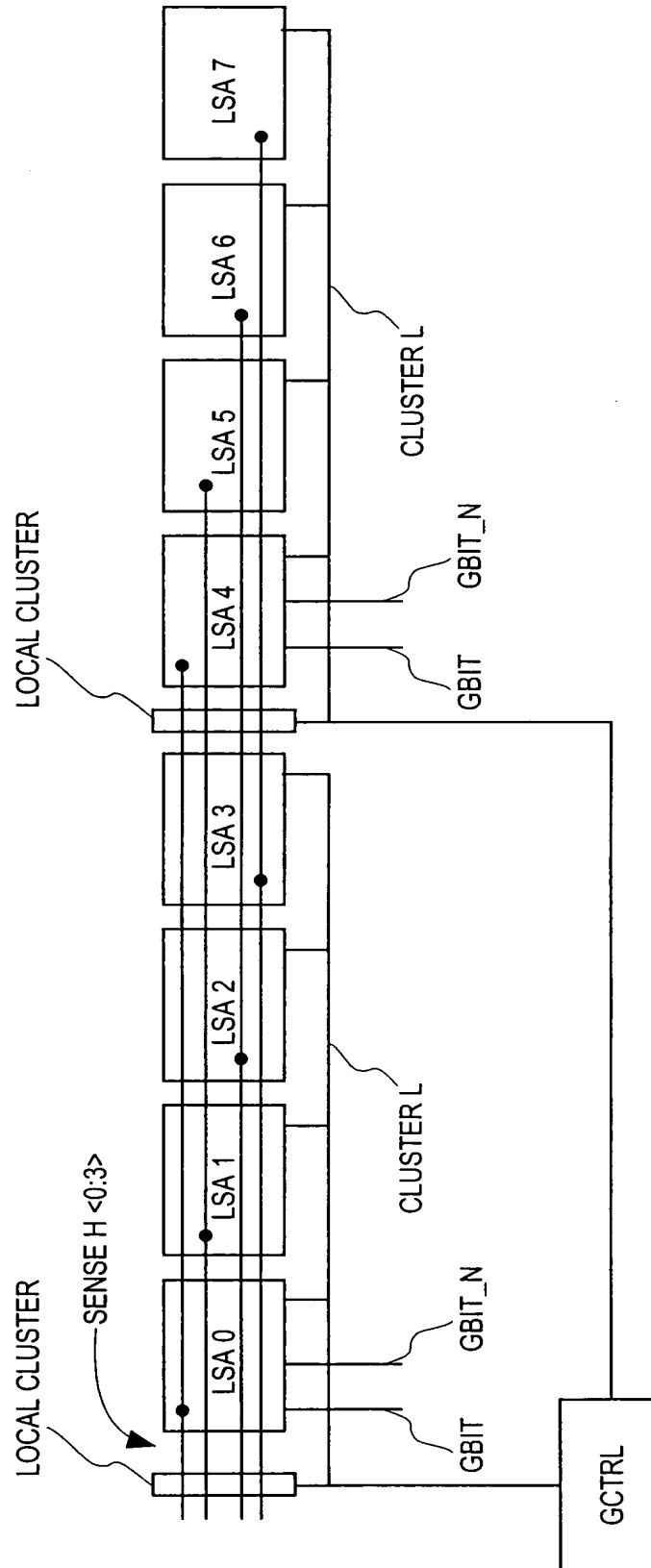


FIG. 23

